655 Montgomery Street, Suite 1800 San Francisco, Ca 94111 TEL 415.318.1160 FAX 415.693.0194

Docket No.: SNDK.308US0

September 17, 2003

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Enclosed herewith for filing is a patent application, as follows:

Inventors:

Raul-Adrian Cernea, Yan Li, Mehrdad Mofidi and Shahzad Khalid

Title:

Non-Volatile Memory and Method With Bit Line to Bit Line Coupled Compensation

- X Check in the amount of \$1,408.00X Return Receipt Postcard
- X This Transmittal Letter (in duplicate)
 30 pages Specification(not including claims)
- 7 pages Claims
 1 page Abstract
 16 Sheets of Drawings

Other:

Applicant(s) assert(s) entitlement to small entity status for the attached patent application

CLAIMS AS FILED

<u>For</u> Total Claims	Number <u>Filed</u> 41	-20	=	Number <u>Extra</u> 21	x	Rate \$18.00	=	\$ \$	Basic Fee <u>750.00</u> 378.00
Independent Claims	3	-3	=	0	х	\$84.00	=	\$	0.00
Fee of \$280.00 for the first filing of one or more multiple dependent claims per application								\$	280.00
 ☐ Total fee for filing the patent application in the amount of ☐ The Commissioner is hereby authorized to charge any additional fees, which may be 								\$ be	<u>1,408.00</u>
required, or credit any overpayment to Deposit Account 502664.									

EXPRESS MAIL LABEL NO:

EV321716430US

Respectfully submitted,

Philip Yau

Agent for Applicants Reg. No. 32,892